

10/524646

DT05 Rec'd PCT/PTO

17 FEB 2005

#3

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICATION OF: Walter Fix et al.
SERIAL NO: Not assigned GROUP ART UNIT: Not assigned
FILED: Herewith EXAMINER: Not assigned
CUSTOMER NO.: 27162
FOR: Organic Component for Overvoltage Protection and Associated Circuit
ATTY/DKT NO.: 411000-127

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

DISCLOSURE STATEMENT UNDER 37 CFR 1.56

SIR:

This paper is to bring to the attention of the PTO the following U.S. pending applications, all of which are related in different respects to organic electronic devices and/or method of making such devices such as transistors, diodes, integrated circuits and the like. Many of these applications also may have one or more common inventors.

<u>Application No.</u>	<u>Title</u>	<u>Inventors</u>	<u>Atty. Dkt. No.</u>
10/332,140	Method for the Production and Configuration of Organic Field-Effect Transistors (OFET)	Adolf Bernds et al.	411000-103
10/344,951	Organic Field-Effect Transistor (OFET), A Production Method Therefor, An Integrated Circuit Constructed From the Same and Their Uses	Adolf Bernds et al.	411000-99

10/362,932	Organic Field Effect Transistor, Method for Structuring an OFET and Integrated Circuit	Adolf Bernds et al	411000-110
10/380,113	Organic Rectifier, Circuit, RFID Tag and Use of an Organic Rectifier	Adolf Bernds et al.	411000-106
10/380,206	Organic Memory, Identification Marker (RFID-TAG) with Organic Memory and Uses of an Organic Memory	Adolf Bernds et al.	411000-102
10/381,032	Electrode and/or Conductor Track for Organic Components and Production Method Thereof	Adolf Bernds et al.	411000-105
10/433,959	Organic Field Effect Transistor, Method For Structuring an OFET and Integrated Circuit	Adolf Bernds	411000-108
10/433,961	Device For Detecting and/or Transmitting at Least One Environmental Influence, Method for Producing Said Device and Use Thereof	Wolfgang Clemens et al.	411000-111
10/467,636	Organic Field Effect Transistor With a Photostructured Gate Dielectric, Method for the Production and Use Thereof in Organic Electronics	Adolf Bernds et al.	411000-104
10/473,050	Device With At Least Two Organic Electronic Components and Method for Producing the Same	Adolf Bernds et al.	411000-113
10/479,234	Organic Field Effect Transistor, Method for Production and Use Thereof in the Assembly of Integrated Circuits	Adolf Bernds et al.	411000-101
10/479,238	Method For Producing Conductive Structures by Means of Printing Technique, and Active Components Produced Therefrom For Integrated Circuits	Adolf Bernds et al.	411000-100
10/492,922	Insulator for An Organic Electronic Component	Erwann Guillet et al.	411000-115
10/492,923	Electronic Unit, Circuit Design for the Same and Production Method	Wolfgang Clemens et al.	411000-114
10/498,610	Organic Field Effect Transistor with Offset Threshold Voltage and the Use Thereof	Walter Fix et al.	411000-119

<u>Application No.</u>	<u>Title</u>	<u>Inventors</u>	<u>Atty. Dkt. No.</u>
10/508,640	Logic Component Comprising Organic Field Effect Transistors	Walter Fix et al.	411000-120
10/508,737	Device and Method for Laser Structuring Functional Polymers and the Uses Thereof	Adolf Bernds et al.	411000-121
10/517,750	Substrate for an Organic Field Effect Transistor, Use of the Substrate, Method of Increasing the Charge Carrier Mobility and Organic Field Effect Transistor (OFET)	Wolfgang Clemens et a.	411000-122
	Electronic Component Comprising Predominantly Organic Functional Materials and a Process for the Production Thereof	Adolf Bernds et al.	411000-123
	Electronic Device	Wolfgang Clemens et al.	411000-124

The Commissioner is authorized to charge payment of any fees associated with this communication or credit any overpayment to Deposit Account No. 03-0678.

Respectfully submitted,
Walter Fix et al.


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#248159

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SIR:

Pursuant to 37 C.F.R. §1.56(a), Applicant(s) hereby cite(s) the enclosed documents listed on the attached copy of Form PTO-1449 which are believed to be material to the patentability of the above-identified application.

This Information Disclosure Statement is filed in accordance with the paragraph of 37 CFR §1.97 indicated below:

- X §1.97(b) This Information Disclosure Statement is filed:
- (1) Within three months of the filing date of a national application; OR
 - (2) Within three months of the date of entry of the national stage of an international application; OR
 - (3) Before the mailing of a first Office Action on the merits.
No fee or statement is required.

 §1.97(c) This Information Disclosure Statement is filed after the period specified in paragraph (b) above, but before the mailing date of either:

- (1) A Final Action or under 37 CFR §1.113; OR
- (2) A Notice of Allowance under 37 CFR §1.311; AND

is accompanied by either: (check one)

- The statement as specified in 37 CFR §1.97(e) set out below; OR
 The fee of \$180.00 under 37 CFR §1.17(p).

___§1.97(d) This Information Disclosure Statement is filed after the mailing date of either:

- (1) a Final Action or under 37 CFR §1.113; OR
- (2) A Notice of Allowance under 37 CFR §1.311;

BUT filed on or before payment of the Issue Fee; AND
is accompanied by:

- (1) The statement as specified in 37 CFR §1.97(e) as set forth below; AND
- (2) Petition is hereby made under 37 CFR §1.97(d) for consideration of this Information Disclosure Statement; AND,
- (3) The petition fee of \$180.00 set out in 37 CFR §1.17(i).

___§1.97(e) The undersigned Attorney hereby states that:

___ each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing date of this Information Disclosure Statement;

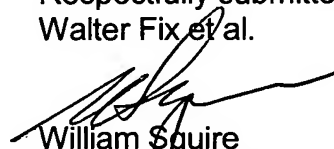
or

___ no item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, or to the knowledge of the undersigned Attorney after making reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing date of the Information Disclosure Statement.

This Information Disclosure Statement provides a detailed list of the references cited in various pending applications listed in applicant's Disclosure Statement filed herewith.

The Commissioner is authorized to charge payment of any fees associated with this communication or credit any overpayment to Deposit Account No. 03-0678.

Respectfully submitted,
Walter Fix et al.



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Fax: (973)994-1744

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				Complete if Known	
				Application Number	Not assigned
				Filing Date	Herewith
				First Named Inventor	Walter Fix
				Group Art Unit	Not assigned
				Examiner Name	Not assigned
Sheet	1	Of	12	Attorney Docket Number	411000-127

U.S. PATENT DOCUMENTS						
Examiner Initial*	Cite No. ¹	Document Number ----- Number-Kid Code ² (if known)	Publication- Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
	1	US-5,574,291	11/12/1996	Dodabalapur et al.	See Disclosure Statement filed herewith for all references cited	
	2	US-5,206,525	04/27/1993	Yamamoto et al.		
	3	US-4,937,119	06/26/1990	Nickles et al.		
	4	US-5,625,199	04/29/1997	Baumbach et al.		
	5	US-5,705,826	01/06/1998	Aratani et al.		
	6	US-5,854,139	12/29/1998	Kondo et al.		
	7	US-5,883,397	03/16/1999	Isoda et al.		
	8	US-5,998,805	12/07/1999	Shi et al.		
	9	US-6,045,977	04/04/2000	Chandross et al.		
	10	US-6,072,716	06/06/2000	Jacobsen et al.		
	11	US-6,197,663	03/06/2001	Chandross et al.		
	12	US-6,384,804	05/07/2002	Dodabalapur et al.		
	13	US-6,555,840	04/29/2003	Hudson et al.		
FOREIGN PATENT DOCUMENTS						
Examiner Initial*	Cite No. ¹	Foreign Patent Document ----- Country Code ³ Number ⁴ Kind Code ⁵ (if known)	Publication- Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
	14	EP 0460242	12/11/1991			X
	15	WO 93 16491	08/19/1993			X
	16	WO 99 40631	0812/1999			X
	17	WO 99 54936	10/28/1999			X
	18	WO 96 02924	02/01/1996			X
	19	WO 95 31831 title page only	11/23/1995	Philips Electronics		X

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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	20	US-6,221,553	04/24/2001	Wolk et al.	
	21	US-6,150,668	11/21/2000	Bao et al.	
	22	US-6,362,509	03/26/2002	Hart	
	23	US-6,329,226	12/11/2001	Jones et al.	
	24	US-6,593,690	07/15/2003	McCormick et al.	
	25	US-6,498,114	12/24/2002	Amundson et al.	
	26	US-6,603,139	08/05/2003	Tessler et al.	
	27	US-6,621,098	09/16/2003	Jackson et al.	
	28	US-4,340,657	07/20/1982	Rowe	
	29	US-5,364,735	11/15/1994	Akamatsu et al.	
	30	US-6,330,464	12/11/2001	Colvin et al.	
	31	US-5,691,089	11/25/1997	Smayling	
	32	US-6,321,571	11/27/2001	Themont et al.	

FOREIGN PATENT DOCUMENTS						
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	33	WO 99 30432	06/17/1999			X
	34	EP 0 108650	05/16/1984			X
	35	EP 0 418504	03/27/1991			X
	36	DE 198 51703	05/04/00			
	37	WO 98 18186 title page only	04/30/1998			X
	38	DE 10033112 title page only	01/24/2002	Siemens AG		X

Examiner Signature		Date Considered	
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	39	US-6,087,196	07/11/2000	Sturm et al.	
	40	US-6,133,835	10/17/2000	De Leeuw et al.	
	41	US-5,970,318	10/19/1999	Choi et al.	
	42	US-5,321,240	06/14/1994	Takahira	
	43	US-5,973,598	10/26/1999	Beigel	
	44	US-5,347,144	09/13/1994	Garnier et al.	
	45	US-6,207,472	03/27/2001	Calligari et al.	
	46	US-5,486,851	01/23/1996	Gehner et al.	
	47	US-2002/0053320	05/09/2002	Duthaler	
	48	US-2004/0084670	05/06/2004	Tripsas et al.	
	49	US-6,251,513	06/26/2001	Rector et al.	

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	50	EP 0 786820	07/30/1997			X
	51	WO 99 39373	08/05/1999			X
	52	WO 02/47183	06/13/2002			X
	53	EP 1 103916 (title page only)	05/30/2001			
	54	EP 0 528662				
	55	EP 0 442123				
	56	WO 01 17041	03/08/2001	E. Ink Corp.		X

Examiner Signature	Date Considered
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	57	US-2004/0211329	10/00/2004	Funahata et al.	
	58	US-2004/0013982	01/00/2004	Jacobson et al.	
	59	US-2003/0175427	09/00/2003	Loo et al.	
	60	US-6,517,995	02/00/2003	Jacobsen et al.	
	61	US-6,322,736	11/00/2001	Bao et al.	
	62	5,546,889	08/20/1996	Wakita et al.	

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	63	EP 0962984	12/08/99			X
	64	WO 02 19443	03/07/02			X
	65	WO 00 79617	12/28/04			X
	66	WO 99 10939	03/04/99			X
	67	DE 100 43204	04/04/2002			
	68	EP 0 511807	11/04/1992			X
	69	EP 0 979715	02/16/2000			
	70	EP 1 048 912	11/02/2000			
	71	DE 4243832	06/30/94			
	72	EP 0716458	06/12/1996			X
	73	WO 99 21233	04/29/1999			X
	74	WO 01 15233	03/01/2001			X
Examiner Signature				Date Considered		

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Sheet	5	12	Attorney Docket Number	411000-127

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	75	US-5,652,645	07/29/1997	Jain	
	76	US-2002/02284	02/21/2002	Heeger et al.	
	77	US-			
	78	US-			
	79	US-			
	80	US-			

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Examiner Initial*	Cite No. ¹	Foreign Patent Document Country Code ³ Number ⁴ Kind Code ⁵ (if known)	Publication- Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
	81	WO 98/40930	09/17/98			X
	82	DE 19933757	01/25/01			
	83	FR 2793089	11/03/2000			
	84	WO 01 47045	06/28/2001			X
	85	WO 94/17556	0804/1994			X
	86	WO 99/07189	02/11/1999			X
	87	WO 97/18944	05/29/1997			X
	88	EP 0685985	12/06/1995			X
	89	WO 00/36666	06/22/2000			X
	90	DE 100 12204 (title page)	09/20/2001			
	91	DE 198 16 860	11/18/99			
	92	WO 02/065557	08/22/2002			X
	93	DE 10061297 (title page)	06/27/2002			
Examiner Signature				Date Considered		

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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Sheet	6		12	Attorney Docket Number	411000-127

U.S. PATENT DOCUMENTS					
Examiner Initial*	Cite No. ¹	Document Number Number-Kid Code ² (if known)	Publication- Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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FOREIGN PATENT DOCUMENTS						
Examiner Initial*	Cite No. ¹	Foreign Patent Document Country Code ³ Number ⁴ Kind Code ⁵ (if known)	Publication- Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
	94	JP 05259434	10/05/1993			X
	95	WO 99/10929 (title page only)	03/04/1999			X
	96	DE 199 35 527	02/08/2001			
	97	DE 199 37 262	03/01/2001			
	98	WO 01/27998 (title page only)	04/19/2001			X
	99	JP 05152560 (abstract)	06/18/1993			X
	100	DE 10061297 (title page only)	06/27/2002			
	101	EP1224999 (title page only)	07/24/2002			
	102	JP 362065477A abstract	03/24/1987	Toshiba Corp.		X
	103	JP 08197788 abstract	08/06/1996	Hitachi Koki Co.		X
	104	DE 19851703	05/04/2000	Ins. fur Halbleiterphysik		
	105					

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				Application Number	Not assigned
				Filing Date	Herewith
				First Named Inventor	Walter Fix
				Group Art Unit	Not assigned
				Examiner Name	Not assigned
Sheet	7		12	Attorney Docket Number	411000-127

NON PATENT LITERATURE DOCUMENTS
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Examiner Initial*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	106	GARNIER F et al.; "Vertical Devices Architecture By Molding Of Organic-Based Thin Film Transistor", Applied Physics Letters, American Institute Of Physics. XP000784120, issn: 0003-6951 abbildung 2	X
	107	COLLET J. et al.; 'LOW VOLTAGE, 30 NM CHANNEL LENGTH, ORGANIC TRANSISTORS WITH A SELF-ASSEMBLED MONOLAYER AS GATE INSULATING FILMS', APPLIED PHYSICS LETTERS, AMERICAN INSTITUTE OF PHYSICS. NEW YORK, US, Bd 76, Nr. 14, 3. april 2000 (2000-04-03), Seiten 1941-1943, XP000950589, ISSN:0003-6951, das ganze Dokument	X
	108	HWANG J D et al.; "A Vertical Submicron Sic thin film transistor", Solid State Electronics, Elsevier Science Publishers, Barking, GB, Bd. 38, NR. 2.1. February 1995 (1995-02-01), Seiten 275-278, XP004014040, ISSN:0038-1101, Abbildung 2	X
	109	ROGERS J A et al.; "Low-Voltage 0.1 Mum Organic Transistors and Complementary Inverter Circuits Fabricated with a Low-Cost Form of Near-Field Photolithography", Applied Physics Letters, American Institute of Physics. New York, US, Bd. 75, Nr. 7, 16. August 1999 (1999-08-16), Seiten 1010-1012, XP000934355, ISSN: 003-6951, das ganze Dokument	X
	110	"Short-Channel Field-Effect Transistor", IBM Technical Disclosure Bulletin, IBM Corp., New York, US, Bd. 32, Nr. 3A, 1.August 1989 (1989-08-01), Seiten 77-78, XP000049357, ISSN:0018-8689, das ganze Dokument	X
	111	REDECKER, M. et al.; "Mobility enhancement through homogeneous nematic alignment of a liquid-crystalline polyfluorene", 1999 American Institute of Physics, Applied Physics Letters, Vol. 74, number 10, pp. 1400-1402.	X
	112	ROGERS, J. A. et al.; "Printing Process Suitable for Reel-to-Reel Production of High-Performance Organic Transistors and Circuits", Advanced Materials, VCH, Verlagsgesellschaft, Weinheim, DE, Bd. 11, Nr. 9, 5. Juli 1999 (1999-07-05), Seiten 741-745, P000851834, ISSN: 0935-9648, das ganze Dokument	X
	113	MIYAMOTO, Shoichi et al.; "Effect of LDD Structure and Channel Poly-Si Thinning on a Gate-All-Around TFT (GAT) for SRAM's, IEEE Transactions on Electron Devices. Vol. 46, No. 8, August 1999	X
	114	KUMAR, Anish et al.; "Kink-Free Polycrystalline Silicon Double-Gate Elevated-Channel Thin-Film Transistors", IEEE Transactions on Electron Devices, Vol. 45, No. 12, December 1998	X
	115	CHEN, Shiao-Shien et al.; "Deep Submicrometer Double-Gate Fully-Depleted SOI PMOS Devices: A Concise Short-Channel Effect Threshold Voltage Model Using a Quasi-2D Approach", IEEE Transaction on Electron Devices, Vol. 43, No. 9, September 1996	X

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				Group Art Unit	Not assigned
Examiner Name	Not assigned				
Sheet	8		12	Attorney Docket Number	411000-127

NON PATENT LITERATURE DOCUMENTS

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	116	ZANGARA L: "Metall Statt Halbleiter, Programmierung Von Embedded ROMS Ueber Die Metallisierungen", Elektronik, Franzis Verlag GMBH, Munchen, DE, Bd. 47, Nr. 16, 4 August 1998 (1998-08-04), Seiten 52-55, XP000847917, ISSN: 0013-5658, Seite 52, rechtes Plate, Zeile 7-Seite 53, linke Spalte, Zeile 14; Abbildungen 1, 2	
	117	HERGEL, H. J.: "Pld-Programmiertechnologien", Elektronik, Franzis Verlag GMBH, Munchen, DE, Bd 41, Nr. 5, 3. Marz 1992 (1992-03-03), Seiten 44-46, XP000293121, ISSN: 0013-5658, Abbildungen 1-3.	
	118	FORREST et al.: "The Dawn of Organic Electronics", IEEE Spectrum, August 2000 (2000-08), Seiten 29-34, XP002189000, IEEE Inc., New York, US ISSN:0018-9235, Seite 33, rechte Spalte, Zelle 58-Seite 34, linke Spalte, Zeile 24; Abbildung 5.	X
	119	PATENT ABSTRACTS OF JAPAN, Vol. 009, No. 274 (E-354), 31 Oktober 1985 (1985-10-31) & JP 60 117769 A (Fujitsu KK), 25 Juni 1985 (1985-06-25) Zusammenfassung	X
	120	ZIE VOOR TITEL BOEK, de 2e PAGINA, XP-002189001, PG 196-228.	X
	121	DRURY et al., "Low-Cost All-Polymer Integrated Circuits", American Institute of Physics, Applied Physics Letters, 1998, Vol. 73, No. 1, pp 108-110, July 6, 1998.	X
	122	KUHLMANN et al., "Terabytes in Plastikfolie", Organische Massenspeicher vor der Serienproduktion	
	123	GARNIER, F. et al, "All-Polymer Field-Effect Transistor Realized by Printing Techniques", Science, American Association for the Advancement of Science, US, vol 265, 16 September 1994, pp 1684-1686.	X
	124	ASSADI A, et al., "Field-Effect Mobility of Poly (3-Hexylthiophene) Dept. of Physics and Measurement Technology, Received 3 March 1988; accepted for Publication 17 May 1988	X

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	125	BAO, Z. et al., "High-Performance Plastic Transistors Fabricated by Printing Techniques", Chem. Mater Vol. 9, No. 6, 1997, pp 1299-1301.	X
	126	HEBNER, T.R. et al., "Ink-jet printing of doped polymers for organic light emitting devices", American Institute of Physics, Applied Physics Letters, Vol. 72, no. 5, February 2, 1998, pages 519-521.	X
	127	ANGELOPOULOS M et al, "In-Situ Radiation Induced Doping", Mol. Cryst. Liq. Cryst. 1990, vol. 189, pp. 221-225.	X
	128	DAI, L. et al, "Photochemical Generation of Conducting Patterns in Polybutadiene Films", Macromolecules, Vol. 29, No. 1, 1996, pages 282-287, XP 001042019, the whole document	X
	129	ROMAN et al., "POLYMER DIODES WITH HIGH RECTIFICATION", Applied Physics Letters, Vol. 75, No. 21, November 22, 1999	X
	130	PATENT ABSTRACTS OF JAPAN, Vol. 010, No. 137, May 21, 1986 (JP 361001060A)	X
	131	SCHOEBEL, "Frequency Conversion with Organic-On-Inorganic Heterostructured Diodes", Extended Abstracts of the International Conference on Solid State Devices and Materials, September 1, 1997	X
	132	BRAUN D., et al, "Visible light emission from semiconducting polymer diodes", American Institute of Physics, Applied Physics Letters 58, May 6, 1991, pages 1982 - 1984.	X
	133	YU, G. et al., "Dual-function semiconducting polymer devices: Light-emitting and photodetecting diodes", American Institute of Physics, Applied Physics Letter 64, March 21, 1994, pages 1540 -1542.	X
	134	LUCENT TECHNOLOGIES, "Innovation marks significant milestone in the development of electronic paper", Cambridge, MA and Murray Hill, NJ, November 20, 2000. XP-002209726.	X
	135	CLEMENS, W. et al., "Vom Organischen Transistor Zum Plastik-Chip," Physik Journal, V. 2, 2003, pp. 31-36.	X

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	136	GARNIER et al., "Conjugated Polymers and Oligomers as Active Material For Electronic Devices", Synthetic Metals, Vol. 28, 1989	X
	137	GELINCK, G.H. et al., "High-Performance All-Polymer Integrated Circuits", Applied Physics Letters, v. 77, 2000, pp. 1487-1489.	X
	138	ZHENG, Xiang-Yang et al., "Electrochemical Patterning of the Surface of Insulators with Electrically Conductive Polymers", J. Electrochem. Soc., v. 142, 1995, pp L226-L227.	X
	139	LIDZEY, D. G. et al., "Photoprocessed and Micropatterned Conjugated Polymer LEDs", Synthetic Metals, V. 82, 1996, pp. 141-148	X
	140	KOBEL W. et al., "Generation of Micropatterns in Poly (3-Methyl-Thiophene) Films Using Microlithography: A First Step in the Design of an All-Organic Thin-Film Transistor" Synthetic Metals, V. 22, 1988, pp. 265-271.	X
	141	ROST, Henning et al., "All-Polymer Organic Field Effect Transistors", Proc. Mat. Week, CD, 2001, pp. 1-6	X
	142	MANUELLI, Alessandro et al., "Applicability of Coating Techniques for the Production of Organic Field Effect Transistors", IEEE Polytronic 2002 Conference, 2002, pp. 201-204.	X
	143	ULLMAN, A. et al., "High Performance Organic Field-Effect Transistors and Integrated Inverters", Mat. Res. Soc. Symp. Proc., v. 665, 2001, pp. 265-270.	X
	144	FIX, W. et al., "Fast Polymer Integrated Circuits Based on a Polyfluorene Derivative", ESSDERC 2002, 2002, pp. 527-529.	X
	145	KNOBLOCH, A. et al., "Printed Polymer Transistors", Proc. Polytronic, v. 84, 2001, pp. 84-89	X
	146	FICKER, J. et al., "Dynamic and Lifetime Measurements of Polymer OFETS and Integrated Plastic Circuits, " Proc. of SPIE, v. 466, 2001, pp. 95-102	X
	147	PATENT ABSTRACTS OF JAPAN, vol 013, no. 444 (E-828), 5 Oktober 1989 (1989-10-05) & JP 01 169942 A (Hitachi Ltd), 5 July 1989	

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	148	CRONE, B. ET AL, "Large-scale complementary Integrated circuits based on Organic transistors", Nature, Vol. 403, Feb. 3, 2000, PP. 521 -	X
	149	FIX, W., et al., "Fast polymer integrated circuits", American Institute of Physics, Applied Physics Letters, Vol. 81, No. 89, August 2002, pp. 1735-1737.	X
	150	DODABALAPUR, A. et al., "Organic smart pixels", American Institute of Physics, Applied Physics Letters, Vol. 73, No. 2, July 13, 1998, pp. 142 - 144.	X
	151	HALLS, J.J. M., et al., "Efficient photodiodes from interpenetrating polymer networks", Nature, Vol. 376, August 10, 1995, pp. 498 - 500.	X
	152	BROWN, A.R. et al., "Field-effect transistors made from solution-processed organic semiconductors", Elsevier Science, S.A., Synthetic Metals 88 (1997) pp. 37-55	X
	153	BROWN, A.R., "Logic Gates Made from Polymer Transistors and Their Use in Ring Oscillators", Science, Vol. 270, November 10, 1995, pp 972 - 974	X
	154	KLAUK, H. et al., "Pentacene Thin Film Transistors and Inverter Circuits", 1997 International Electron Devices Meeting Technical Digest, pages 539-542, December 1997	X
	155	KLAUK, H. et al., "Fast Organic Thin Film Transistor Circuits", IEEE Electron Device Letters, Vol. 20, no. 6, pages 289-291	X
	156	BAO, Z. et al., "Organic and Polymeric Materials for the Fabrications of Thin Film Field-Effect Transistors", paper presented at the meeting of American Chemical Society, Division of Polymer Chemistry, XX, XX, Bd. 39, Nr. 1, 29 Marz 1998 (1998-03-29), P001032497, ISSN: 0032-3934 das ganze Dokument	X
	157	SCHRODNER M. ET AL., "Plastic electronics based on Semiconducting Polymers", First International IEEE Conference on Polymers and Adhesives in Microelectronics and Photonics. Incorporating Poly, Pep & Adhesives in Electronics. Proceedings (Cat. No. 01TH8592), First International IEEE Conference on Polymers and Adhesives in Micr, Seitenn 91 - 94.	

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	158	OELKRUG, D. et al., "Electronic spectra of self-organized oligothiophene films with 'standing' and 'lying' molecular units", Elsevier Science S.A., 1996, Thin Solid Films 284-270	X
	159	SANDBERG, H. et al, "Ultra-thin Organic Films for Field Effect Transistors", SPIE Vol. 4466, 2001, pp. 35 – 43.	X
	160	CHEN, X.L. et al., "Morphological and Transistor Studies of Organic Molecular Semiconductors with Anisotropic Electrical Characteristics", American Chemical Society, 2001, Chem. Mater. 2001, 13, 1341—1348.	X
	161	DE LEEUW D.M. et al., "Polymeric integrated circuits and light-emitting diodes", Electron Devices Meeting, 1997. Technical Digest, International, Washington, DC, USA 7-10 Dec. 1997, New York, NY, USA, IEEE, US 7 December 1997.	X
	162	LOWE, J. et al., "Poly (3—(2—Acetoxyethyl)Thiophene): A Model Polymer for Acid-Catalyzed Lithography", Synthetic Metals, Elsevier Sequoia, Lausanne, CH, Bd. 85, 1997, Seiten 1427-1430.	X
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